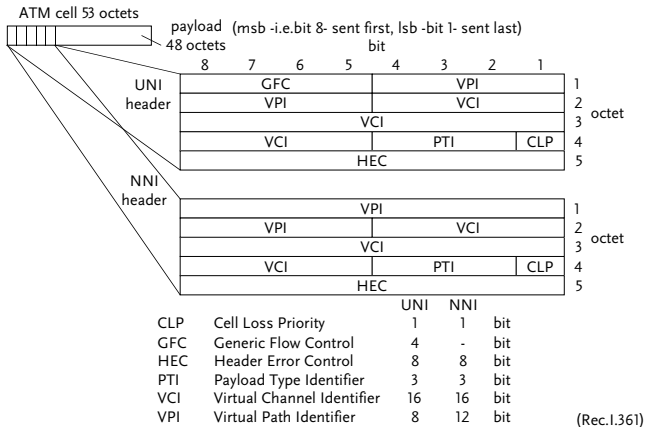


ATM cell header structure at UNI & NNI



Cell types

Standardised physical layer cell header values at the UNI. Note that the last bit in octet 4 is not used as CLP for physical layer cells. Physical layer cells are not passed to the ATM layer.

	Octet 1	Octet 2	Octet 3	Octet 4
Idle cell	0000	0000	0000	0000
Reserved for phys. layer	PPPP	0000	0000	0000
OAM cell (F1)	0000	0000	0000	0000
OAM cell (F3)	0000	0000	0000	1000

Combinations of pre-assigned values at the UNI. The first 4 bits of octet 1 are for GFC. Use

Use	VPI	VCI	PTI	CLP
Meta-signalling	XXXX	XXXX	0000	0000
General broadcast signalling	XXXX	XXXX	0000	0000
Point-to-point signalling	XXXX	XXXX	0000	0101
VP OAM flow (F4)	YYYY	YYYY	0000	0000
VP end-to-end OAM (F4)	YYYY	YYYY	0000	0100
VP resource management	YYYY	YYYY	0000	0110
Reserved for VP functions	YYYY	YYYY	0000	0111
VC OAM flow (F5)	YYYY	YYYY	ZZZZ	ZZZZ
VC end-to-end OAM (F5)	YYYY	YYYY	ZZZZ	ZZZZ
Resource mgmt. cell	YYYY	YYYY	ZZZZ	ZZZZ
Reserve for future standardisation	YYYY	YYYY	0000	1000
Unassigned cell	0000	0000	0000	0000

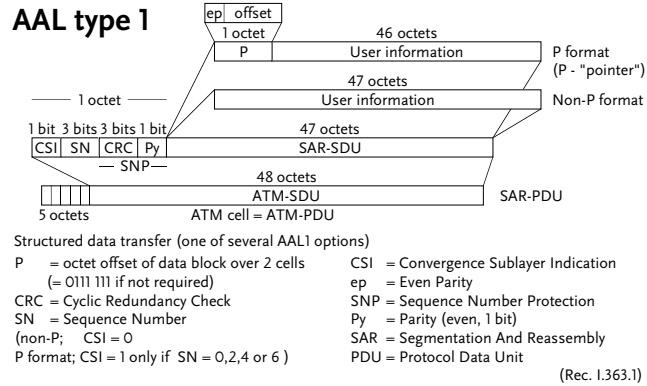
- A- The bit may be 0 or 1 and is available for use by the appropriate ATM layer functions
 - B- The bit is a "don't care" bit
 - C- The originating signal entity shall set CLP to 0. The value may be changed by the network.
 - P- The bit is available for use by the physical layer
 - X- Any VPI value VPI=0; VCI value is reserved for user signalling with local exchange VPI≠0; VCI value is reserved for signalling with other signalling entities
 - Y- Any VPI value
 - Z- Any VCI value other than 0
 - ?. For further study
- (Rec. 1.361)

PTI usage

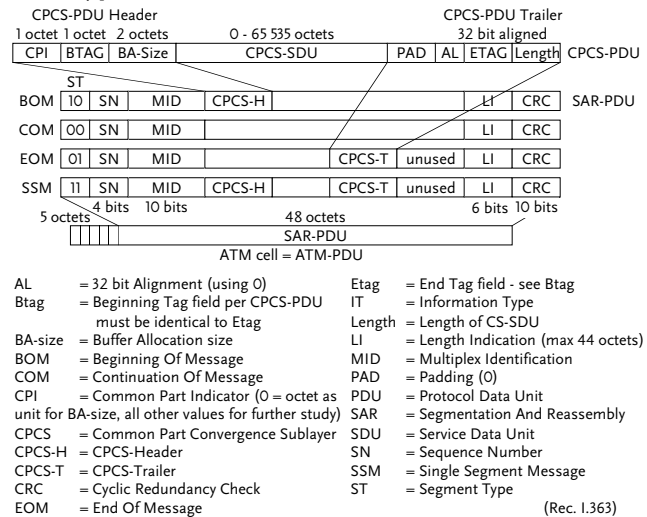
ATM user data cell	no congestion experienced	ATM layer user-user indication=0	PTI
ATM user data cell	no congestion experienced	ATM layer user-user indication=1	000
ATM user data cell	congestion experienced	ATM layer user-user indication=0	001
ATM user data cell	congestion experienced	ATM layer user-user indication=1	010
OAM (F5) cell associated with segment			011
OAM (F5) cell associated with end-to-end Resource management cell			100
Reserved for future functions			101
			110
			111

(Rec. 1.361)

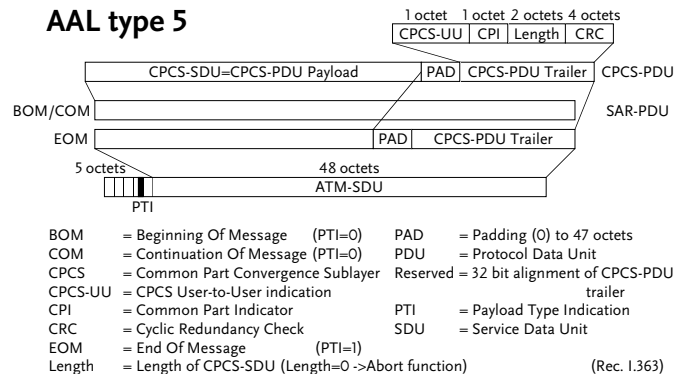
AAL type 1



AAL type 3/4



AAL type 5

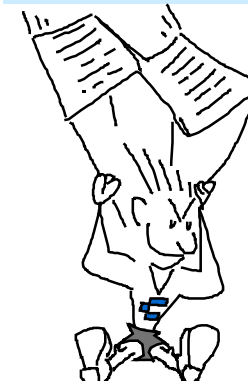


cellware broadband

Rudower Chaussee
12489 Berlin, Germany

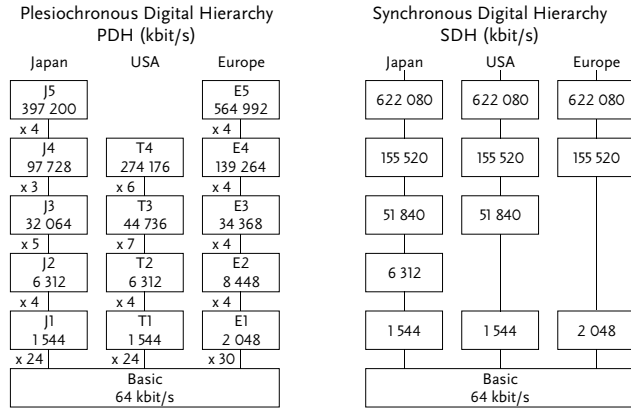
Phone (+49 30) 6 70 08 -0
Fax (+49 30) 6 70 08 - 108
Email info@cellware.de
www <http://www.cellware.de>

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Not enough? Try <http://www.cellware.de> for more.

Digital network hierarchies - bit rates



SONET/SDH - bit rates

CCITT (ITU-T) STM-M Synchronous Transport Module level M	ANSI STS-N Synchronous Transport Signal level N	Bit-Rate (Mbit/s)
-	STS-1	51.84
STM-1	STS-3 & STS-3c	155.52
-	STS-9	466.56
STM-4	STS-12	622.08
-	STS-18	933.12
-	STS-24	1 244.16
-	STS-36	1 866.24
STM-16	STS-48	2 488.32

SONET/SDH - optical

Application	Intra-office	Interoffice			
		Short Haul		Long Haul	
Wavelength	1310 nm	1310 nm	1550 nm	1310 nm	1550 nm
Fibre type	G.652	G.652	G.652	G.652	G.652/G.654 G.653
Range	<2km	≈15km	≈40km	≈60km	

G.703 physical/electrical characteristics

E1	2 048 kbit/s	±50 ppm	HDB3	75Ω coaxial 120Ω twisted pair
E2	8 448 kbit/s	±30 ppm	HDB3	75Ω coaxial
E3	34 368 kbit/s	±20 ppm	HDB3	75Ω coaxial
E4	139 264 kbit/s	±15 ppm	CMI	75Ω coaxial
SDH	155 520 kbit/s	±20 ppm	CMI	75Ω coaxial

ATM Forum phys./elec. characteristics

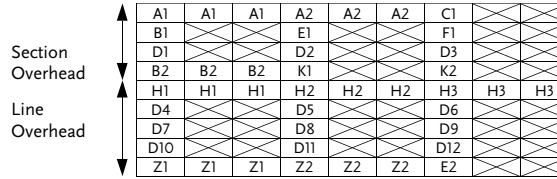
1	STS-3c	155.52 Mbit/s		optical fibre
2	DS3	44.74 Mbit/s	B3ZS	75Ω coaxial
3	100Multi	100.00 Mbit/s	4B/5B	1300 nm 62.5/125 μm multimode optical fibre
4	155Multi	155.52 Mbit/s	8B/10B	1300 nm 62.5/125 μm multimode optical fibre

"Other" phys./elec. characteristics

TAXI	140 Mbit/s	4B/5B	1300 nm	62.5/125 μm multimode optical fibre
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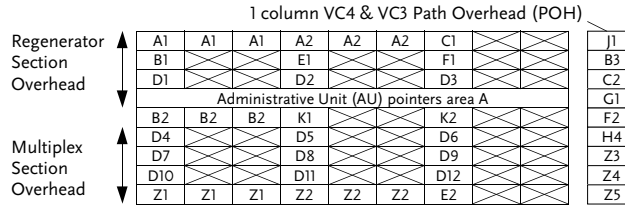
Not enough? Try <http://www.itu.ch/> for more.

SONET STS-3 transport & path overhead



SDH STM-1 section & path overhead

(STS-3C almost same as STM-1, except for 2 bits in H1)

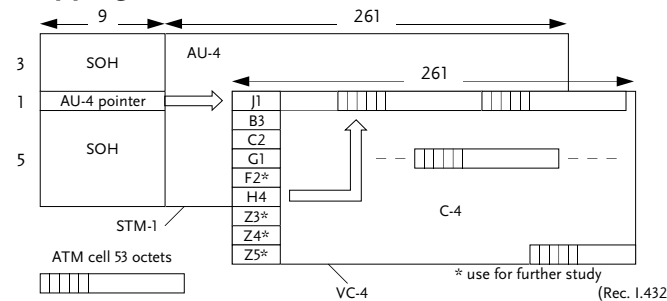


- Reserved for national use or undefined (all zeroes pattern)
- | | | | |
|--------|---|-------|-------------------------------------|
| A1,A2 | Framing 11110110 00101000 | Z1,Z2 | future functions (to be defined) |
| B1 | Bit Interleaved Parity (BIP-8) | B3 | Path Bit Interleaved Parity (BIP-8) |
| B2 | Bit Interleaved Parity (BIP-24) | C2 | Signal label |
| C1 | STM identifier | F2 | VC-n Path user channel |
| D1-D12 | Data communication over section | G1 | Path status |
| E1,E2 | Orderwire (voice communication) | H4 | Multiframe indicator |
| F1 | User channel | J1 | VC-n Path trace |
| K1,K2 | Automatic Protection Switching (APS) signalling | Z3-5 | future functions (to be defined) |
- (Rec. G.708)

Scrambling

STM signal scrambled with frame synchronous generator polynomial $1 + x^6 + x^7$. (Rec.G.709)

Mapping of ATM cells into an STM-1



H4, for ATM cell mapping into SDH

unused	unused	Cell offset indicator (0 ≤ offset ≤ 52)							
1	2	m	3	4	5	6	7	l	8

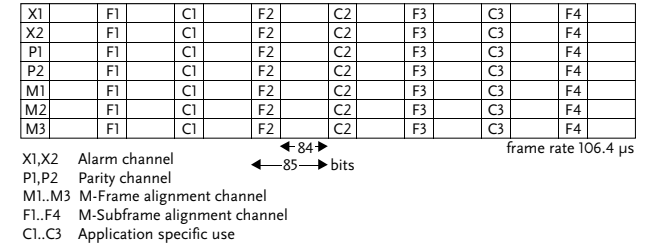
N.B. This pointer is generally redundant as the HEC mechanism for cell-delineation provides the same function with better performance. (Rec. I.432)

Scrambling

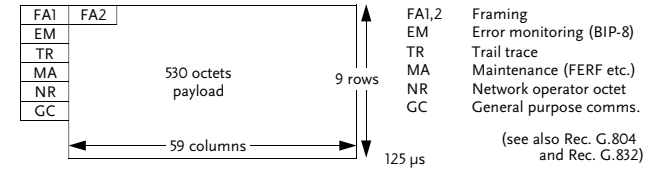
ATM cell payload scrambled with self synchronising generator polynomial $x^43 + 1$. (Rec. I.432)

Not enough? Try <http://www.atmforum.com/> for more.

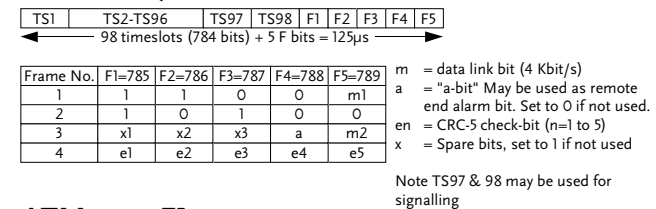
ATM over DS3



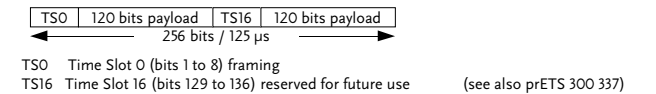
ATM over E3



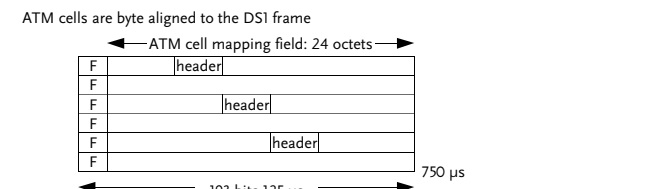
ATM over J2



ATM over E1



ATM over DS1



F bits provide F3 OAM functions For framing detail see ATT pub.43801 (D4 or 193S) or ESF (193E)

Detection of loss of frame alignment Performance monitoring (CRC-6) Transmission of RDI Performance reporting (see also Rec. G.804)

ATM cell delineation

ATM cell delineation using HEC field - generator polynomial $g(x) = x^8 + x^2 + x + 1$ (Rec.I.432)